



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Sang-Eun Lee and Jae-Sung Han

Serial No. 10/068,152

Examiner: Hollington, Jermele M.

Confirmation No.: 7500

Filed: February 6, 2002

Group Art Unit: 2829

For: METHOD OF IDENTIFYING AND
ANALYZING SEMICONDUCTOR
CHIP DEFECTS

Mail Stop NON FEE AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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22313-1450
Date: July 7, 2003

Christina Lawton
Christina Lawton

Responsive to the Office Action dated April 10, 2003, enclosed is an amendment in the
above-identified application.

The fee has been calculated as shown below.

CLAIMS AS AMENDED					
For:	Number After Amendment	Previous Number	Extra	Rate	Additional Fee
Total Claims	20	-20*	0	x \$18 =	\$0
Independent Claims	3	-3**	0	x \$84 =	\$0
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0

*greater of twenty (20) or number for which fee has been paid

**greater of three (3) or number for which fee has been paid



Two sheets of corrected drawings (FIG. 1 and FIG. 4A)



Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.



20575

PATENT TRADEMARK OFFICE

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PATENT APPLICATION
Docket No. 8750-017
Client No. SPX200110010-US

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MAIL STOP NON FEE AMENDMENT

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P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT

Responsive to the Office Action dated April 10, 2003, please amend the application as follows.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Drawings begin on page 3 of this paper.

Amendments to the Claims are found in the listing of claims that begins on page 4 of this paper.

Remarks/Arguments begin on page 5 of this paper.

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Date: July 7, 2003

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